

ABSTRACT OF THE DISCLOSURE

A display panel has: a plurality of pixels; a plurality of scanning lines; a plurality of signal lines through each of which a multiplexed data signal for first to third
5 color components is transmitted; and a plurality of demultiplexers, each of which includes first to third demultiplexing switch elements respectively controlled by first to third demultiplex control signals, one end of each of the demultiplexing switch elements being connected to one of the signal lines and the other end of each of the demultiplexing switch elements being connected to a pixel for the j -th color component
10 ($1 \leq j \leq 3$, j is an integer) among the plurality of pixels. A gate signal generation circuit generates a shift clock signal based on the first to third demultiplex control signals, shifts a start pulse signal based on the shift clock signal to obtain a shift output, and outputs a signal corresponding to the shift output to one of the scanning lines.